Amendments to the Specification

Please replace the paragraph starting on line 4 page 1 with the following amended paragraph:

This application claims the benefit of U.S. Provisional Application Serial Number 60/266,025 filed on February 2, 2001 and is a continuation of issued U.S. Patent Number 6,647,055 filed on May 18, 2001 which is a Continuation –In-Part of [co-pending U.S. Patent Application Serial Number 09/652,936] issued U.S. Patent Number 6,507,604 filed on August 31, 2000.

Please replace the paragraph starting on line 9 page 7 with the following amended paragraph:

Referring to FIG. 3 there is shown a block diagram representation of the resultant rake receiver. The pilot channel can be the pilot code channel or the pilot symbols. The architecture of the present invention is equally well suited for application to a rake receiver of any CDMA systems. The received signal is processed by a signal processor 300 having a corresponding demodulator 302 for each transmission path 1 through L. The demodulator 302 for path l contains a de-spreader for the pilot channel 304 in path l and a de-spreader for the traffic channel 306 in the path l. A variance estimator 308 and a complex conjugate function 310 are coupled to the output of the de-spreader for the pilot channel 304. The output of the de-spreader for the traffic channel 306 and the output of the complex conjugate function 310 are input to multiplier 312. Function Re[x] 314, which is an operation taking the real part of x, is coupled to the output of multiplier 312. An inversion function 316 is coupled to the output of variance estimator 308. The output

of the inversion function 316 and the output of function Re[x] are coupled to the inputs of

multiplier 318. The output of multiplier 318 is coupled to adder 320. Corresponding

adders 320 combine the corresponding outputs of the demodulators for paths 1 through L.